

OCT 10 2006 10:38a

Verity Fort Collins

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P. 1



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7590 07/14/2006

AGILENT TECHNOLOGIES, INC.
 Legal Department, DL429
 Intellectual Property Administration
 P.O. Box 7599
 Loveland, CO 80537-0599
 10/11/2006 RMEBRAH1 00000016 10815521

01 FC:1501 1400.00 OP
 02 FC:1501 APPLICATION NO. 10/815,521 FILING 04/01/2004

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Valerie Bakes	(Depositor's name)
Valerie Bakes	(Signature)
October 10, 2006	(Date)

FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
Andrew S. Hildebrand	10031350-1	1566

10/815,521

04/01/2004

Andrew S. Hildebrand

10031350-1

1566

TITLE OF INVENTION: VERIFICATION OF INTEGRATED CIRCUIT TESTS USING TEST SIMULATION AND INTEGRATED CIRCUIT SIMULATION WITH SIMULATED FAILURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	10/16/2006
EXAMINER	ART UNIT		CLASS-SUBCLASS			
DOAN, NGUYEN M	2825		716-004000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

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Singapore

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Authorized Signature

Typed or printed name

Regan L. Trumper

Date October 10, 2006

Registration No. 38345

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